

NOTE 4

	PIN POSITION			PIN POSITION	
PIN	Х	Y	PIN	Х	Υ
1	-16.75	11.25	12	16.75	-6.55
2	-13.85	11.25	13	15.25	-11.25
3	-8.45	11.25	14	12.35	-11.25
4	-5.95	11.25	15	5.35	-11.25
5	2.85	11.25	16	2.85	-11.25
6	5.35	11.25	17	-5.95	-11.25
7	12.35	11.25	18	-8.45	-11.25
8	15.25	11.25	19	-13.85	-11.25
9	16.75	6.55	20	-16.75	-11.25
10	16.75	4.05	21	-16.75	-3.25
11	16.75	-4.05	22	-16.75	3.25

**DATE 21 MAY 2019** 

## NOTES:

- 1. DIMENSIONING AND TOLERANCING PER. ASME Y14.5M, 2009.
- 2. CONTROLLING DIMENSION: MILLIMETERS
- 3. DIMENSION 6 APPLIES TO THE PLATED TERMINALS AND IS MEASURED BETWEEN 1.00 AND 3.00 FROM THE TERMINAL TIP.
- 4. POSITION OF THE CENTER OF THE TERMINALS
  IS DETERMINED FROM DATUM B THE CENTER OF
  DIMENSION D, X DIRECTION, AND FROM DATUM A,
  Y DIRECTION. POSITIONAL TOLERANCE, AS NOTED
  IN DRAWING, APPLIES TO EACH TERMINAL IN BOTH
  DIRECTIONS.
- PACKAGE MARKING IS LOCATED AS SHOWN ON THE SIDE OPPOSITE THE PACKAGE ORIENTATION FEATURES.

	MILLIMETERS				
DIM	MIN.	N□M.	MAX.		
Α	13.50	13.70	13.90		
A1	0.10	0.20	0.30		
A2	11.50	11.70	11.90		
АЗ	15.65	15.85	16.05		
A4	15.95 REF				
b	1.61	1.66	1.71		
D	54.80	55.00	55.20		
D1	65.60	65.90	66.20		
Ε	32.20	32.50	32.80		
Р	4.20	4.30	4.40		
P1	8.90	9.00	9.10		

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DESCRIPTION:	PIM22 55x32.5 (PRESSFIT PIN)		PAGE 1 OF 2	

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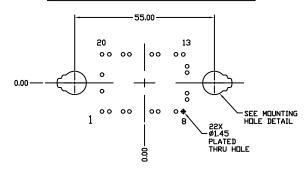
## PIM22 55x32.5 (PRESSFIT PIN)

CASE 180BF ISSUE O

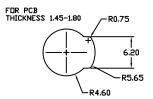
**DATE 17 MAY 2019** 

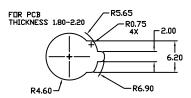
## MOUNTING HOLE POSITION

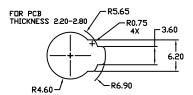
	HOLE POSITION			PIN POSITION	
PIN	Х	Υ	PIN	Х	Υ
1	-16.75	-11.25	12	16.75	6.55
2	-13.85	-11.25	13	15.25	11.25
3	-8.45	-11.25	14	12.35	11.25
4	-5.95	-11.25	15	5.35	11.25
5	2.85	-11.25	16	2.85	11.25
6	5.35	-11.25	17	-5.95	11.25
7	12.35	-11.25	18	-8.45	11.25
8	15.25	-11.25	19	-13.85	11.25
9	16.75	-6.55	20	-16.75	11.25
10	16.75	-4.05	21	-16.75	3.25
11	16.75	4.05	22	-16.75	-3.25



RECOMMENDED MOUNTING PATTERN

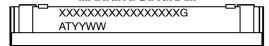






MOUNTING HOLE DETAIL

## GENERIC MARKING DIAGRAM\*



XXXXX = Specific Device Code

G = Pb-Free Package

AT = Assembly & Test Site Code

YYWW = Year and Work Week Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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DESCRIPTION:	PIM22 55x32.5 (PRESSFIT PIN)		PAGE 2 OF 2	

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