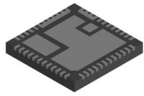


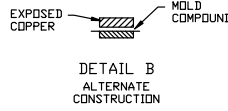
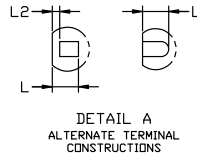
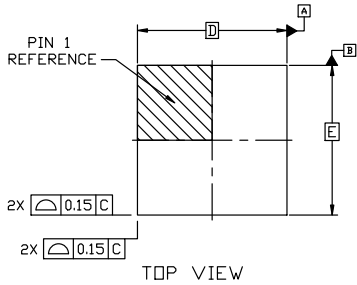
# MECHANICAL CASE OUTLINE

## PACKAGE DIMENSIONS



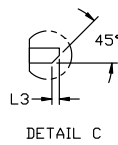
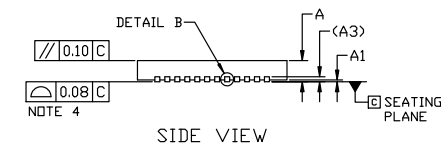
**PQFN48 6.00x6.00x0.75, 0.40P**  
**CASE 483BT**  
**ISSUE F**

DATE 28 JUL 2023

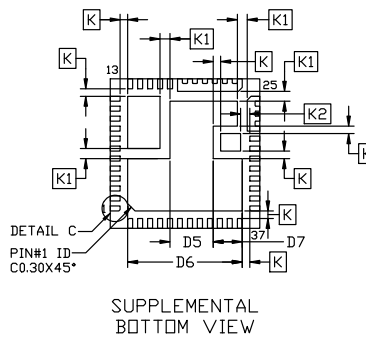
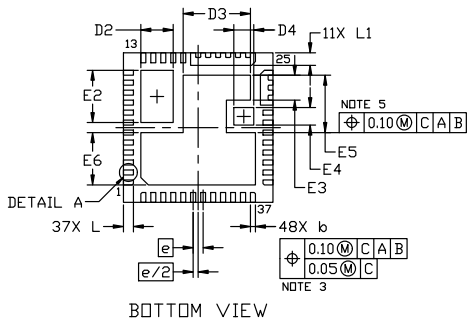


NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5, 2009.
2. CONTROLLING DIMENSION: MILLIMETERS
3. DIMENSION *b* APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 FROM THE TERMINAL TIP.
4. COPLANARITY APPLIES TO THE EXPOSED PADS AS WELL AS THE TERMINALS.
5. POSITIONAL TOLERANCE APPLIES TO ALL THREE EXPOSED PADS IN BOTH X AND Y AXIS.



DIM	MILLIMETERS		
	MIN.	NDM.	MAX.
A	0.70	0.75	0.80
A1	---	---	0.05
A3	0.20 REF.		
<i>b</i>	0.15	0.20	0.25
D	5.90	6.00	6.10
D2	1.20	1.30	1.40
D3	2.60	2.70	2.80
D4	0.70	0.80	0.90
D5	1.63	1.73	1.83
D6	4.50	4.60	4.70
D7	1.07	1.17	1.27
E	5.90	6.00	6.10
E2	2.00	2.10	2.20
E3	0.90	1.00	1.10
E4	0.60	0.70	0.80
E5	2.20	2.30	2.40
E6	2.00	2.10	2.20
<i>e</i>	0.40 BSC		
K	0.30 REF		
K1	0.40 REF		
K2	0.37 REF		
L	0.30	0.40	0.50
L1	0.40	0.50	0.60
L2	---	---	0.15
L3	0.15 REF		



### GENERIC MARKING DIAGRAM\*



XXXX = Specific Device Code  
 A = Assembly Location  
 WL = Wafer Lot  
 Y = Year  
 WW = Work Week

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

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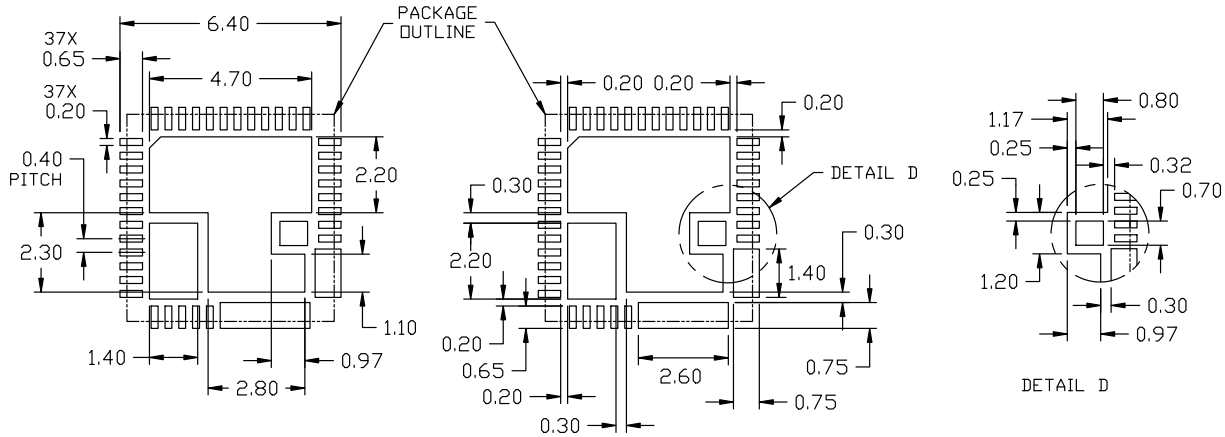
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**MECHANICAL CASE OUTLINE**  
**PACKAGE DIMENSIONS**

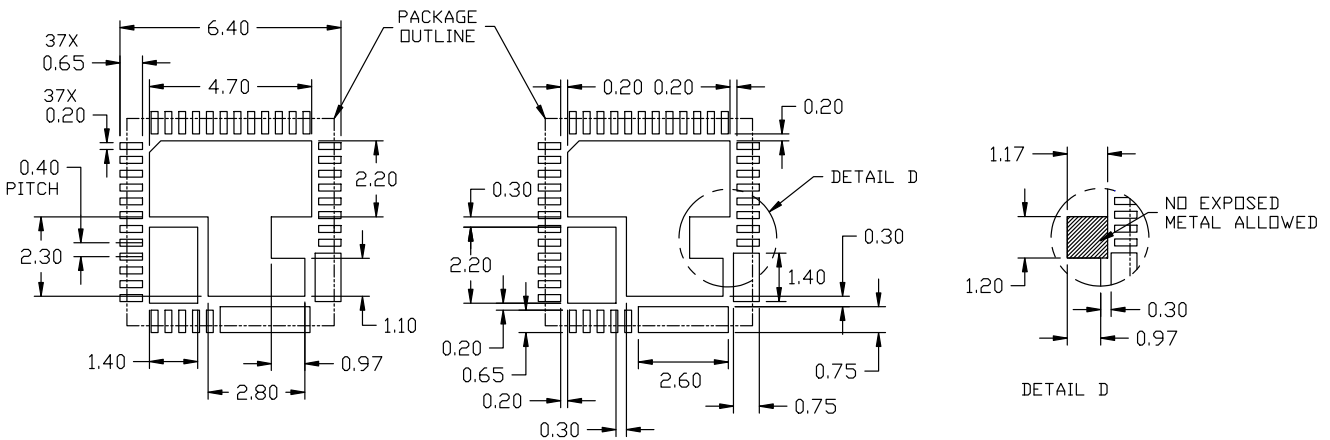


**PQFN48 6.00x6.00x0.75, 0.40P**  
**CASE 483BT**  
**ISSUE F**

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RECOMMENDED MOUNTING FOOTPRINT\*  
 (1.5X SCALE)



RECOMMENDED MOUNTING FOOTPRINT - ALTERNATE\*  
 (1.5X SCALE)

\* For additional information on our Pb-Free strategy and soldering details, please download the ONSEMI Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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