-[A] В





SCALE 2.5:1

0.10 C

0.10 C

PIN 1 REFERENCE

PQFN31 5X5, 0.5P CASE 483BR ISSUE D

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MILLIMETERS

0.75

NOM. MAX.

0.80

- 1.
- DDES NOT FULLY CONFORM TO JEDEC REGISTRATION MD-220, DATES MAY/2005.
 DIMENSIONING AND TOLERANCING PER ASME Y14.5, 2009.
 CONTROLLING DIMENSION MILLIMETERS
 DIMENSIONS DO NOT INCLUDE BURRS AND SMEAR OR MOLD FLASH

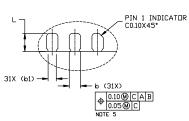
DIM

Α

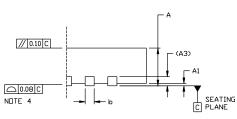
- DIMENSIONS DO NOT INCLUDE BORRS AND SMEAR OR MILLS
 FLASH.
 MOLD FLASH OR BURRS AND SMEAR DO NOT EXCEED 0.10MM.
 DIMENSION 6 AND BI APPLIES TO PLATED TERMINAL AND IS
 MEASURED BETWEEN 0.15 AND 0.30 FROM THE TERMINAL TIP.

MIN.

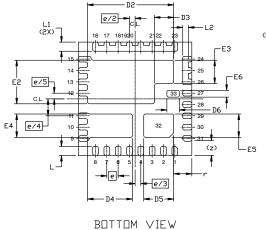
0.70



DETAIL 'B'



DETAIL <u>'A'</u> (SCALE 2:1)

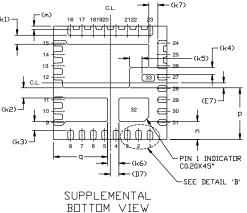


TOP VIEW

SIDE VIEW

محمحملية

SEE DETAIL 'A'



A1	0.00	-	0.05	
A3	0.15	0.20	0.25	
b	0.20	0.25	0.30	
b1	0.13	0.18	0.30	
D	4.90	5.00	5.10	
D2	3.70	3.80	3.90	
D3	0.75	0.85	0.95	
D4	1.88	1.98	2.08	
D5	1.22	1.32	1.42	
D6	0.45	0.55	0.65	
D7	0.38 REF			
Е	4.90	5.00	5.10	
E2	1.82	1.92	2.02	
E3	0.93	1.03	1.13	
E4	0.93	1.03	1.13	
E5	0.93	1.03	1.13	
E6	0.20	0.30	0.40	
E7	0.22 REF			
е	0.50 BSC			
e/2	0.25 BSC			
e/3	0.25 BSC			
e/4	0.75 BSC			
e/5	0.25 BSC			
k1	0.40 REF			
k2	0.45 REF			
k3	0.40 REF			
k4	0.30 REF			
k5	0.55 REF			
k6	0.50 REF			
k7	0.40 REF			
L	0.30	0.40	0.50	
L1	0.30	0.40	0.50	
L2	0.15	0.25	0.35	
m	0.15 REF			
n	0.80 REF			
р	2.28 REF			
q	2.38 REF			
r	0.80 REF			
z	0.625 REF			

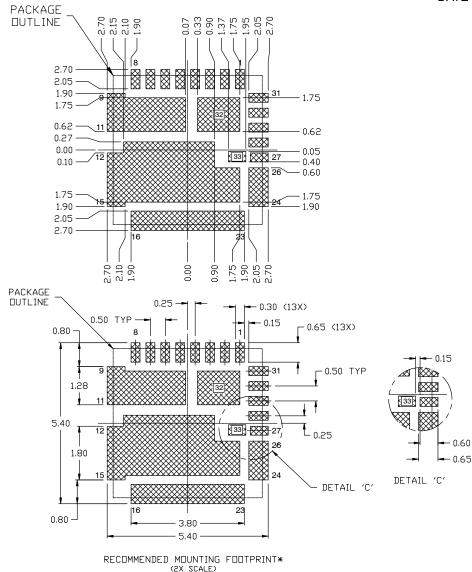
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* For additional information on our Pb-Free strategy and soldering details, please download the DNSEMI Soldering and Mounting Techniques Reference Manual, SDLDERRM/D.

GENERIC MARKING DIAGRAM*

XXXXXXXX XXXXXXXX AWLYYWW• XXXX = Specific Device Code
A = Assembly Location

WL = Wafer Lot YY = Year WW = Work Week

= Pb-Free Package(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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