

DATE 19 NOV 2019

NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- 2. CONTROLLING DIMENSION: MILLIMETERS.
- 3. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR BURRS. MOLD FLASH PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.150mm PER SIDE.
- 4. DIMENSIONS D AND E ARE DETERMINED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY.
- 5. DATUMS A AND B ARE DETERMINED AT DATUM PLANE H.

UNIT IN MILLIMETER				
DIM	MIN	NOM	MAX	
Α	1.10	1.20	1.30	
A1	0.00	0.08	0.15	
A2	1.10	1.15	1.20	
A3	0.25 REF			
A4	0.45	0.50	0.55	
b	0.40	0.45	0.50	
b2	3.80	4.10	4.40	
b3	2.00	2.10	2.20	
b4	0.70	0.80	0.90	
b5	0.55	0.65	0.75	
b6	0.31 REF			
С	0.19	0.22	0.25	
c2	0.19	0.22	0.25	
D	4.05	4.15	4.25	
D1	3.80	4.00	4.20	
D2	3.00	3.10	3.20	
D3	0.30	0.40	0.50	
D4	0.90	1.00	1.10	
Е	4.80	4.90	5.00	
E1	3.10	3.20	3.30	
E2	5.00	5.15	5.30	
е	1,27 BSC			
1/2e	0.635 BSC			
e1	0.40 REF			
I	6.00	6.15	6.30	
L	0.40	0.65	0.85	
L1	0.80	0.90	1.00	
L2	0.90	1.10	1.30	
L3	0.00	0.10	0.20	
q	0°	4°	8°	

RECOMMENDED LAND PATTERN

*FOR ADDITIONAL INFORMATION ON OUR PB-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

GENERIC MARKING DIAGRAM*

BOTTOM VIEW



XXXXXX = Specific Device Code A = Assembly Location

A = Assembly Loca
WL = Wafer Lot

Y = Year W = Work Week

*This information is generic. Please refer to device data sheet for actual part marking. Some products may not follow the Generic Marking.

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